

L Number	Hits	Search Text	DB	Time stamp
1	2387021	memory address counter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:06
2	1603527	memory tester\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:06
3	5098916	memory under test	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:06
4	1650373	(memory tester\$1) or (memory under test)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:07
5	9946	((memory address counter\$1) and ((memory tester\$1) or (memory under test)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:08
6	2216438	error capture memory or ECM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:10
7	7724	((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:10
8	1269328	row\$1 and column\$1 near1 ECM cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:11
9	1382	((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM) and (row\$1 and column\$1 near1 ECM cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:11
10	834333	detect\$3 and (defective memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:11
11	701	((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM) and (row\$1 and column\$1 near1 ECM cell\$1) and (detect\$3 and (defective memory cell\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:12
12	796759	counter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:12
13	310	((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM) and (row\$1 and column\$1 near1 ECM cell\$1) and (detect\$3 and (defective memory cell\$1)) and counter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:12
14	7681584	input control data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:12

14	4108117	test result\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:48
15	145	(((((memory tester\$1) and ((memory array and (row\$1 near1 columns with memory cell\$1)) and (row address or column address))) and counter\$1) and (counter monitor\$1)) and (error capture memory or (ECM))) and ((detect\$3 or determin\$3) and (defective memory cell\$1))) and (test result\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:48
16	3179596	fail data	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:49
17	103	(((((memory tester\$1) and ((memory array and (row\$1 near1 columns with memory cell\$1)) and (row address or column address))) and counter\$1) and (counter monitor\$1)) and (error capture memory or (ECM))) and ((detect\$3 or determin\$3) and (defective memory cell\$1))) and (test result\$1) and (fail data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:49
18	1273324	ECM cell\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:49
19	69	(((((memory tester\$1) and ((memory array and (row\$1 near1 columns with memory cell\$1)) and (row address or column address))) and counter\$1) and (counter monitor\$1)) and (error capture memory or (ECM))) and ((detect\$3 or determin\$3) and (defective memory cell\$1))) and (test result\$1) and (fail data) and (ECM cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:54
20	990280	generat\$3 and (number\$1 near1 defective memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:55
21	67	(((((memory tester\$1) and ((memory array and (row\$1 near1 columns with memory cell\$1)) and (row address or column address))) and counter\$1) and (counter monitor\$1)) and (error capture memory or (ECM))) and ((detect\$3 or determin\$3) and (defective memory cell\$1))) and (test result\$1) and (fail data) and (ECM cell\$1) and (generat\$3 and (number\$1 near1 defective memory cell\$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:55
22	13976	714/718 or 714/704 or 714/706 or 365/200 or 365/201	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:58
23	47	(((((memory tester\$1) and ((memory array and (row\$1 near1 columns with memory cell\$1)) and (row address or column address))) and counter\$1) and (counter monitor\$1)) and (error capture memory or (ECM))) and ((detect\$3 or determin\$3) and (defective memory cell\$1))) and (test result\$1) and (fail data) and (ECM cell\$1) and (generat\$3 and (number\$1 near1 defective memory cell\$1)) and (714/718 or 714/704 or 714/706 or 365/200 or 365/201)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 10:58

15	149	(((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1))) and counter\$1) and (input control data)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:12
17	1165184	counter monitor\$3 and (fail data with defective memory cell\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:14
19	4644	714/718 or 714/706 or 714/704	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:22
20	0	(714/718 or 714/706 or 714/704) and ((((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1))) and counter\$1) and (input control data)) and (counter monitor\$3 and (fail data with defective memory cell\$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:16
18	149	(((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1))) and counter\$1) and (input control data)) and (counter monitor\$3 and (fail data with defective memory cell\$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:23
21	0	(714/718 or 714/706 or 714/704) and ((((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1))) and counter\$1) and (input control data))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:22
22	1	(714/718 or 714/706 or 714/704) and ((((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1))) and counter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:25
23	3	(714/718 or 714/706 or 714/704) and ((((((memory address counter\$1) and ((memory tester\$1) or (memory under test))) and (error capture memory or ECM)) and (row\$1 and column\$1 near1 ECM cell\$1)) and (detect\$3 and (defective memory cell\$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:27
24	5057	(detect\$3 and (defective memory cell\$1)) and ((memory address counter\$1) and ((memory tester\$1) or (memory under test)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:28
25	17	(714/718 or 714/706 or 714/704) and ((detect\$3 and (defective memory cell\$1)) and ((memory address counter\$1) and ((memory tester\$1) or (memory under test))))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/01 18:28